Patents

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	
James Crawford Carson	
Serial No.: **10/623,382**	Art Unit: **2817**
Filed: **July 18, 2003**	Examiner: **Dean O. Takaoka**
For: **Double-Sided, Edge-Mounted Stripline Signal Processing Modules And Modular Network**	

RESPONSE TO FIRST OFFICE ACTION

Commissioner for Patents
Mail Stop Fee Amendment
P.O. Box 1450
Alexandria, VA 22313-1450

April 29, 2005

Sir:

AMENDMENT

In response to the telephone conference conducted on April 26, 2005, please enter the claim amendments shown in the concurrently submitted Amendment Dated April 29, 2005 to place the application in condition for allowance.

RECORD OF TELEPHONE INTERVIEW

Undersigned Attorney for Applicant thanks Examiner Takaoka for the courtesy extended during the telephone interview conducted on April 26, 2005. The parties discussed Claim 1 several references including Nishikawa, U.S. Pat. No. 5,634,208, Rosen U.S. Pat. No. 4,879,711 and Koch, 5,032,803.

I hereby certify that this correspondence is being filed with the United States Patent and Trademark Office, Patents by facsimile directed to Examiner Dean O. Takaoka in Art Unit 2817 at (571) 273 1772 on **April 29, 2005.**

Michael J. Mehrman - Reg. No. 40,086

At the conclusion of the conference, there wan an agreement that claims entered in the form shown in the Amendment Dated April 29, 2005 would be allowable. Examiner Takaoka also reviewed and informally approved this amendment during a subsequent telephone conference on April 29, 2005. Applicant has therefore entered this to place the application in condition for allowance.

CONCLUSION

As discussed above, Applicant believes that the claims are in condition for allowance. If Examiner Takaoka believes that there are any issues that can be resolved by a telephone conference, or that there are any informalities that can be corrected by an Examiner's amendment, please call Mike Mehrman at (404) 497-7400.

Respectfully submitted,

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In re Application of:

James Crawford Carson

Serial No.: **10/623,382**

Filed: **July 18, 2003**

For: **Double-Sided, Edge-Mounted Stripline Signal Processing Modules And Modular Network**

Art Unit: **2817**

Examiner: **Dean O. Takaoka**

RESPONSE DATED APRIL 29, 2005

Commissioner for Patents
Mail Stop No Fee Amendment
P.O. Box 1450
Alexandria, VA 22313-1450

April 29, 2005

Sir:

Please enter the following amendments as shown:

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Michael J. Mehrman - Reg. No. 40,086

1. (twice amended) A stripline signal processing module comprising:

a first planar dielectric substrate defining an edge;

a second planar dielectric substrate defining an edge;

a ground plane;

the first dielectric substrate, the second dielectric substrate, and the ground plane adhered together in an overlaying configuration with the ground plane located between the first and second dielectric substrates and the edges aligned to form an interface edge;

a first stripline circuit ~~carried on the~~ exposed on one side to the first dielectric substrate and to air on an opposing side;

a second stripline circuit ~~carried on the~~ exposed on one side to the second dielectric substrate and to a air on an opposing side;

one or more input ports located at the interface edge and electrically connected to the first or second stripline circuits;

one or more output ports located at the interface edge and electrically connected to the first or second stripline circuits; and

the first and second stripline circuits configured to receive propagating signals at the input ports, perform a signal processing operation on the received propagating signals, and deliver processed signals to the output ports~~; and;~~

~~the first and second stripline circuits comprise a signal processing circuit selected from the group consisting of:~~

~~a two-by-four beam steering circuit;~~

~~a diplexer filter circuit comprising at least three ports;~~

~~a four-by-four Butler matrix circuit;~~

~~an eight by eight Butler matrix circuit; and~~

~~a monopulse comparator circuit.~~

2. (original) The stripline signal processing module of claim 1, wherein:

the first dielectric substrate, the second dielectric substrate, and the ground plane are approximately coextensive in their planar dimensions; and

the first and second stripline circuits comprise stripline exposed to the dielectric substrate on one side and exposed to air or a dielectric material on an opposing side.

3. (original) The stripline signal processing module of claim 1, further comprising one or more electrical connections between the first and second stripline circuits.

4. (original) The stripline signal processing module of claim 3, wherein the electrical connections between the first and second stripline circuits comprise tap-through connectors passing through and insulated from the ground plane.

5. (original) The stripline signal processing module of claim 1, wherein the first and second stripline circuits are non-crossing.

6. (original) The stripline signal processing module of claim 4, wherein:

the first stripline circuit defines a first stage orthogonal beam forming network;

the second stripline circuit defines a second stage orthogonal beam forming network; and

the electrical connections between the first and second stripline circuits participate in the formation of crossovers connecting the first and second stage orthogonal beam forming network into a multi-stage orthogonal beam forming network.

7. (twice amended) The stripline signal processing module of claim 1, wherein the first and second stripline circuits comprise ~~a Butler matrix circuit configured for a carrier frequency.~~ a signal processing circuit selected from the group consisting of:

a two-by-four beam steering circuit;

a diplexer filter circuit comprising at least three ports;

a four-by-four Butler matrix circuit;

an eight-by-eight Butler matrix circuit; and

a monopulse comparator circuit.

8. (original) The stripline signal processing module of claim 3, wherein:

the first and second stripline circuits define non-crossing portions of a hybrid junction circuit; and

the electrical connections between the first and second stripline circuits participate in the implementation of one or more crossovers associated with the hybrid junction circuit.

9. (once amended) The stripline signal processing module of claim 1, wherein the first and second stripline circuits define a beam forming network.

10. (original) The stripline signal processing module of claim 1, wherein the first or second stripline circuits comprises one or more sinuous trace legs configured to exhibit a desired phase and impedance characteristic while reducing the displacement of the trace in a selected dimension.

11. (once amended) The stripline signal processing module of claim 7, wherein the planar dimensions of the first dielectric substrate, the second dielectric substrate, and the ground plane are less than one and one-half times the wavelength of the carrier frequency in the stripline.

12. (original) The stripline signal processing module of claim 1, wherein:

the first and second stripline circuits comprise a four-by-four Butler matrix circuit configured for a carrier frequency; and

the planar dimensions of the first dielectric substrate, the second dielectric substrate, and the ground plane include:

a length in the direction of the interface edge that is less than one and one-half times the wavelength of the carrier frequency in the stripline, and

a width perpendicular to the interface edge that is less than one-half times the wavelength of the carrier frequency in the stripline.

13. (original) The stripline signal processing module of claim 1, wherein:

the first and second stripline circuits comprise a four-by-four Butler matrix circuit configured for a carrier frequency; and

the planar dimensions of the first dielectric substrate, the second dielectric substrate, and the ground plane include:

a length in the direction of the interface edge that is approximately equal to the wavelength of the carrier frequency in the stripline, and

a width perpendicular to the interface edge that is approximately one-fourth times the wavelength of the carrier frequency in the stripline.

14. (twice amended) A stripline signal processing module comprising:

a first planar dielectric substrate defining an edge;

a second planar dielectric substrate that is approximately coextensive with the first planar dielectric substrate defining an edge;

a ground plane that is approximately coextensive with the first and second planar dielectric substrates;

the first dielectric substrate, the second dielectric substrate, and the ground plane adhered together in an overlaying configuration with the ground plane located between the first and second dielectric substrates and the edges aligned to form a interface edge;

a first stripline circuit exposed to the first dielectric substrate on one side and exposed to air ~~or a dielectric material~~ on an opposing side;

a second stripline circuit exposed to the second dielectric substrate on one side and exposed to air ~~or a dielectric material~~ on an opposing side;

a plurality of input interface ports located at the interface edge and electrically connected to the first stripline circuit;

a plurality of output interface ports located at the interface edge and electrically connected to the second stripline circuit;

one or more tap-through electrical connections between the first and second stripline circuits passing through and insulated from the ground plane electrical connections; and

the first and second stripline circuits configured to receive propagating signals at the input ports, perform a signal processing operation on the received propagating signals, and deliver processed signals to the output ports ~~and;~~

~~the first and second stripline circuits comprise a signal processing circuit selected from the group consisting of:~~

~~a two-by-four beam steering circuit;~~

~~a diplexer filter circuit comprising at least three ports;~~

~~a four-by-four Butler matrix circuit;~~

~~an eight-by-eight Butler matrix circuit; and~~

~~a monopulse comparator circuit.~~

15. (original) The stripline signal processing module of claim 14, wherein:

the first stripline circuit defines a first stage orthogonal beam forming network;

the second stripline circuit defines a second stage orthogonal beam forming network; and

the electrical connections between the first and second stripline circuits participate in the formation of crossovers connecting the first and second stage orthogonal beam forming network into a multi-stage orthogonal beam forming network.

16. (original) The stripline signal processing module of claim 14, wherein the first or second stripline circuits comprises one or more sinuous trace legs configured to exhibit a desired phase and impedance characteristic while reducing the displacement of the trace in a selected dimension.

17. (once amended) The stripline signal processing module of claim 1, wherein the first and second stripline circuits comprise a Butler matrix circuit configured for a carrier frequency.

18. (once amended) The stripline signal processing module of claim 17, wherein the planar dimensions of the first dielectric substrate, the second dielectric substrate, and the ground plane are less than one and one-half times the wavelength of the carrier frequency in the stripline.

19. (original) The stripline signal processing module of claim 14, wherein:

the first and second stripline circuits comprise a four-by-four Butler matrix circuit configured for a carrier frequency; and

the planar dimensions of the first dielectric substrate, the second dielectric substrate, and the ground plane include:

a length in the direction of the interface edge that is less than one and one-half times the wavelength of the carrier frequency in the stripline, and

a width perpendicular to the interface edge that is less than one-half times the wavelength of the carrier frequency in the stripline.

20. (original) The stripline signal processing module of claim 14, wherein:

the first and second stripline circuits comprise a four-by-four Butler matrix circuit configured for a carrier frequency; and

the planar dimensions of the first dielectric substrate, the second dielectric substrate, and the ground plane include:

a length in the direction of the interface edge that is approximately equal to the wavelength of the carrier frequency in the stripline, and

a width perpendicular to the interface edge that is approximately one-fourth times the wavelength of the carrier frequency in the stripline.

21. (twice amended) A stripline signal processing network comprising:

a double-sided dielectric substrate board having first and second planar dielectric substrate boards ~~surfaces~~ located on opposing sides of a ground plane;

an interface edge defined by at least one ~~side~~ of the dielectric substrate ~~board~~ boards;

a first stripline circuit ~~carried on~~ exposed on one side to the first ~~planar surface~~ dielectric substrate board and to air on an opposing side, the first stripline circuit further defining a first non-crossing portion of the stripline signal processing network;

a second stripline circuit ~~carried on~~ exposed on one side to the second ~~planar surface~~ dielectric substrate board and to air on an opposing side, the second stripline circuit further defining a second non-crossing portion of the stripline signal processing network;

one or more electrical connections between the first and second stripline circuits participating in the formation of a crossover associated with the stripline signal processing network;

one or more input ports and output ports located along the interface edge and electrically connected to the stripline circuits;

the stripline signal processing network configured to receive propagating signals at the input ports, perform a signal processing operation on the received propagating signals, and deliver processed signals to the output ports and;

each lower-order hybrid junction circuit comprises a Butler matrix circuit.

22. (original) The stripline signal processing network of claim 21, wherein:

the stripline circuits comprise stripline segments having sizes selected to exhibit desired phase and impedance characteristics; and

the electrical connections between the first and second stripline circuits comprise tap-through connectors passing through and insulated from the ground plane.

23. (twice amended) A stripline signal processing network constructed from stripline carried on a dielectric substrate board comprising:

a first portion of the stripline signal processing network ~~located on a~~ exposed on one side to the first side of a double-sided dielectric substrate board and to air on an opposing side;

a second portion of the stripline signal processing network circuit ~~located on~~ exposed on one side to a second side of the double-sided dielectric substrate board and to air on an opposing side;

one or more electrical connections between the first and second portions of the stripline signal processing network;

one or more input ports and output ports located along an interface edge defined by the dielectric substrate board; and

the stripline signal processing network configured to receive propagating signals at the input ports and deliver processed signals to the output ports; ~~and~~

~~the signal processing module implementing a signal processing function selected from the group consisting of:~~

~~a diplexer filter circuit comprising at least three ports;~~

~~a four-by-four Butler matrix circuit;~~

~~an eight-by-eight Butler matrix circuit; and~~

~~a monopulse comparator circuit~~.

24. (original) The stripline signal processing network of claim 23, wherein:

the first and second portions of the network are non-crossing; and

the electrical connections between the first and second portions of the network participate in the formation of a crossover associated with the network.

25. (twice amended) A modular stripline signal processing network comprising an interconnected set of network modules, wherein each network module comprises:

a first stripline circuit ~~located on~~ exposed on one side to a first side of a double-sided dielectric substrate board and to air on an opposing side;

a second stripline circuit ~~located on~~ exposed on one side to a second side of the double-sided dielectric substrate board and to air on an opposing side;

one or more input ports and output ports located along an interface edge defined by the dielectric substrate board; and

the network module configured to receive propagating signals at the input ports, perform a signal processing operation on the received propagating signals, and deliver processed signals to the output ports ~~and;~~

~~network module comprises a circuit selected from the group consisting of:~~

~~a two-by-four beam steering circuit;~~

~~a diplexer filter circuit comprising at least three ports;~~

~~a four-by-four Butler matrix circuit;~~

~~an eight-by-eight Butler matrix circuit; and~~

~~a monopulse comparator circuit~~.

26. (original) The modular stripline signal processing network of claim 25, wherein the interface ports for each network module are edge-connected to another network board through soldered connections.

27. (original) The modular stripline signal processing network of claim 25, wherein the interface ports for each network module are configured for removable edge-connection to another network board through separable connections.

28. (original) The modular stripline signal processing network of claim 27, wherein the separable connections comprise blind-mate coaxial connectors.

29. (original) The modular stripline signal processing network of claim 27, wherein:

each network module implements a lower-order hybrid junction circuit; and

the interconnected set of network modules combines the network modules to implement a higher-order hybrid junction circuit.

30. (once amended) The modular stripline signal processing network of claim 29, wherein:

the higher-order hybrid junction circuit includes at least sixteen input ports and sixteen output ports.

31. (original) The modular stripline signal processing network of claim 25, wherein, for each network module, the electrical connections between the first and second stripline circuits comprise tap-through connectors passing through and insulated from the ground plane.

32. (original) The modular stripline signal processing network of claim 25, wherein, for network module:

the first stripline circuit is non-crossing;

the second stripline circuit is non-crossing; and

the electrical connections between the first and second stripline circuits participate in the formation of a crossover associated with the signal processing network.

33. (once amended) The modular stripline signal processing network of claim 25, wherein each network module implements a Butler matrix circuit.

34. (original) The modular stripline signal processing network of claim 25, wherein each network module comprises one or more sinuous trace legs configured to exhibit a desired phase and impedance characteristic while reducing the displacement of the trace in a selected dimension.

35. (twice amended) A modular stripline signal processing network comprising an interconnected set of network modules, wherein each network module comprises:

a first planar dielectric substrate defining an edge;

a second planar dielectric substrate defining an edge;

a ground plane;

the first dielectric substrate, the second dielectric substrate, and the ground plane adhered together in an overlaying configuration with the ground plane located between the first and second dielectric substrates and the edges aligned to form an interface edge;

a first stripline circuit ~~carried on~~ exposed on one side to the first dielectric substrate and to air on an opposing side;

a second stripline circuit ~~carried on~~ exposed on one side to the second dielectric substrate and to air on an opposing side;

one or more electrical connections between the first and second stripline circuits;

one or more input ports located at the interface edge and electrically connected to the first or second stripline circuits;

one or more output ports located at the interface edge and electrically connected to the first or second stripline circuits;

the first and second stripline circuits configured to receive propagating signals at the input ports, perform a signal processing operation on the received propagating signals, and deliver processed signals to the output ports and;

each stripline circuit comprises a Butler matrix circuit.

36. (original) The modular stripline signal processing network of claim 35, wherein, for each network module:

the first stripline circuit define a first stage hybrid junction circuit;

the second stripline circuit define a second stage hybrid junction circuit; and

the electrical connections between the first and second stripline circuits participate in the implementation of one or more crossovers interconnecting the first stage and second stage hybrid junctions into a higher-order orthogonal beam forming network.

37. (once amended) The modular stripline signal processing network of claim 36, wherein the input and output ports for each beam forming network module are edge-connected to another network board.

Respectfully submitted,

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